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САДРЖАЈ CONTENTS

SPECIAL SECTION

ADVANCED WSNs (Wireless Sensor Networks) FOR AGRICULTURAL AND ENVIRONMENTAL APPLICATIONS (WSN)

SS.1.	Kraemer Rolf	
	Wireless Sensor Networks for Use in Agriculture: “Challenges and Potential Solutions” (Invited presentation)	
SS.2.	Stamenković Zoran, Randjić Siniša, Santamaria Ignacio, Pešović Uroš, Panić Goran, Tanasković Snežana	
	<u>Advanced Wireless Sensor Nodes and Networks for Agricultural Applications (Invited paper)</u>	1
SS.3.	Mendez Diego, Diaz Sergio, Kraemer Rolf	
	<u>Wireless Technologies for Pollution Monitoring in Large Cities and Rural Areas</u>	9
SS.4.	Simić Mitar, Stojanović Goran M., Manjakkal Libu, Zaraska Krzysztof	
	<u>Multi-Sensor System for Remote Environmental (Air and Water) Quality Monitoring</u>	15
SS.5.	Piotrowski Krzysztof, Sojka-Piotrowska Anna, Stamenkovic Zoran, Kraemer Rolf	
	<u>IHPNode Platform as a Base for Precision Farming and Remote Diagnosis in Agriculture</u>	19

СЕКЦИЈА 1: ПОЛИТИКА, РЕГУЛАТИВА И УСЛУГЕ У ТЕЛЕКОМУНИКАЦИЈАМА (ПРУ)

SECTION 1: POLICY, REGULATORY AND SERVICES IN TELECOMMUNICATIONS (PRS)

1.1.	Vasić Čedomir, Rančić Dejan	
	<u>Relocation of resources in road transportation based on the use of data from the GPS based systems (Invited paper)</u>	24
1.2.	Chernov Andrey V., Butakova Maria A., Vereskun Vladimir D., Kartashov Oleg O.	
	<u>Situation Awareness Service based on Mobile Platforms for Multilevel Intelligent Control System in Railway Transport</u>	29
1.3.	Atanasova Tatiana, Poryazov Stoyan, Saranova Emiliya	
	<u>Problems with quality enabling of information functions composition in smart buildings</u>	33
1.4.	Guclu Yildiz, Gozuacik Necip, Gul Ahmet	
	<u>Implementing Subscriber-Wide INAP Service in VoIP Switch</u>	37

1.5.	Hajdarevic Kemal, Allen Pat, Spremic Mario <u>Proactive Security Metrics for Bring Your Own Device (BYOD) in ISO 27001 Supported Environments</u>	41
1.6.	Valjarević Aleksandar, Venter Hein, Petrović Ranko <u>ISO/IEC 27043:2015 - Role and application</u>	45
1.7.	Radak Zlata, Radonjić Đogatović Vesna <u>Customized charging option for business customers</u>	49
1.8.	Gabela Adnan <u>Potencijal novih zona poslovanja za telekom operatore u regionu</u>	53
1.9.	Banović-Čurguz Nataša, Ilišević Dijana <u>Evolucija metrika u savremenom telekomunikacionom okruženju</u>	57
1.10.	Asanović Marko, Gospić Nataša <u>Primjena preporuka za Green IT u lokalnim samoupravama</u>	61

СЕКЦИЈА 2: ТЕЛЕКОМУНИКАЦИОНЕ МРЕЖЕ (ТМ) **SECTION 2: TELECOMMUNICATIONS NETWORKS (TN)**

2.1.	Krajnović Nenad <u>Network topologies for achieving maximal all-terminal network availability (Invited paper)</u>	65
2.2.	Trpevski Igor, Kocarev Ljupco <u>Greedy Reduction of Navigation Time in Random Search Processes</u>	70
2.3.	Veletić Mladen, Balasingham Ilanko <u>The Neural Communication Network: A Brief Review on Neuromodeling and Neuroengineering</u>	74
2.4.	Sagatov Evgeny S., Samoilova Daria V., Sukhov Andrei M., Vinogradov Nikita I. <u>Composite distribution for one-way packet delay in the global network</u>	78
2.5.	Redžović Hasan, Smiljanić Aleksandra, Savić Bogdan <u>Performance Evaluation of Software Routers with VPN Features</u>	82
2.6.	Kitanov Stojan, Janevski Toni <u>State of the Art: Fog Computing for 5G Networks</u>	86
2.7.	Poryazov Stoyan, Saranova Emiliya <u>User-oriented, Overall Traffic and Time Efficiency Indicators in Telecommunications</u>	90
2.8.	Ninković Nemanja, Stojanović Mirjana <u>Novel Approaches to Intradomain and Interdomain Quality of Service Enhancement (Invited paper)</u>	94
2.9.	Durković Srđan, Čiča Zoran <u>Birkhoff-von Neumann Switch with Deflection Based Load Balancing</u>	102
2.10.	Vesović Mihailo, Smiljanić Aleksandra, Tomašević Milo <u>Speeding Up IP Lookup Procedure in Software Routers by Means of Parallelization</u>	106

2.11.	Urgun Yilmaz, Kavak Adnan <u>SDN/NFV-based QoS Mechanism for Cellular Core Networks</u>	110
2.12.	Lim Seong-Jin, Chung Jaeho, Jang Jae-Seon <u>Uplink Scheduling Algorithm for Capacity Enhancement in LTE</u>	114
2.13.	Nikookar-Hamedani Pegah, Soleimani-Nasab Ehsan <u>Physical layer security in the presence of the Eavesdroppers in cooperative networks</u>	118
2.14.	Grčić Pukšec Dunja <u>Transforming telco service and resource provisioning</u>	122
2.15.	Veselinović Nenad, Milovanović Marijana, Vasiljević Biljana, Denić Saša <u>Napredne funkcionalnosti nove magistralne OTN/DWDM mreže Telekoma Srbija</u>	126
2.16.	Matic Vladimir <u>Using of post selection delay for evaluation of mixed communication network traffic properties (Invited presentation)</u>	130
2.17.	Al-Dahoud Ali, Fezari Mohamed, Belhouchet Fatma Zohra, Al-Rawashdeh Thamer A. <u>Remote Monitoring System for Solar Power Panels using Intelligent Sensors Network</u>	132
2.18.	Tomovic Slavica, Radusinovic Igor <u>Allocation algorithm for handling multiple applications in software-defined WSN</u>	136
2.19.	Samaras Nicholas <u>Using Basic MANET Routing Algorithms for Data Dissemination in Vehicular Ad Hoc Networks (VANETs)</u>	140
2.20.	Abbaspour-Asadollah Maryam, Soleimani-Nasab Ehsan <u>Performance Analysis of Dual-Hop Fixed-Gain AF Relaying Systems in Wireless Body Area Networks over Gamma Fading Channels</u>	144
2.21.	Alharthi Saad, Johnson Princy <u>Threshold Sensitive Heterogeneous LEACH Protocol for Wireless Sensor Networks</u>	148
2.22.	Enache Dragos, Chenaru Oana, Popescu Dan <u>Performance and Load Analysis for Remote Plant Connectivity Using GSM Communication</u>	152

СЕКЦИЈА 3: РАДИО-КОМУНИКАЦИЈЕ (РК) SECTION 3: RADIO COMMUNICATIONS (RC)

3.1.	Koprivica Mladen, Nešković Nataša, Nešković Aleksandar <u>IMPROVING THE EFFICIENCY OF METHODS FOR MEASUREMENT OF ELECTRIC FIELD STRENGTH IN THE VICINITY OF PUBLIC MOBILE SYSTEM BASE STATIONS (Invited presentation)</u>	156
3.2.	Ozduran Volkan, Soleimani-Nasab Ehsan, Yarman B. Siddik <u>Sum-Rate Based Opportunistic Relay Selection Method For A Dual-Hop Multiple Half/Full-Duplex Bi-directional Wireless Relaying Networks</u>	157
3.3.	Kurnaz Cetin, Korunur Engiz Begum, Albayrak Zafer Emre <u>Determination of TV White Space Spectrum Availability in Samsun Turkey</u>	161
3.4.	Domazetović Bojan, Kočan Enis, Mihovska Alben <u>Performance evaluation of IEEE 802.11ah systems</u>	165
3.5.	Fanan Anwar, Riley N. G., Mehdawi M., Ammar M. <u>Comparison of Propagation Models with Real Measurement around Hull, UK</u>	169
3.6.	Spajic Vlado, Begenisic Djordje, Savkovic Dusan <u>Intra eNB UL CoMP: Concepts, Performance and Field Trial Results</u>	173
3.7.	Djaja-Joško Vitomir, Kolakowski Jerzy <u>Application of Kalman Filter for positioning precision improvement in UWB localization system</u>	177
3.8.	Rosić Maja, Simić Mirjana, Lukić Petar <u>TDOA approach for target localization based on improved genetic algorithm</u>	181
3.9.	Suciu George, Vochin Marius, Vulpe Alexandru, Fratu Octavian <u>Vehicular Mobile Data Collection Platform to Support the Development of Intelligent Transportation Systems</u>	185
3.10.	Mehdawi Mefath, Riley N. G., Fanan A., Ammar M. <u>Influence of Antenna Selection on Spectrum Occupancy Measurements in the Context of Cognitive Radio</u>	189
3.11.	Urošević Uglješa, Veljović Zoran <u>Distributed MIMO solutions for Peer-to-Peer Communications in Future Wireless Systems</u>	193
3.12.	Abdullin Renat R., Sokolov Rostislav I. <u>Experimental Research into Leaky-Wave Antenna Based on Layered Rectangular Waveguide with Transverse Slots</u>	197
3.13.	Saydam Nuri, Yeğin Korkut <u>Ku Band Amplifier Design for Satcom Systems</u>	201
3.14.	Gecan Dragan, Olavsbraten Morten, Gjertsen Karl Martin <u>Investigation of the Gate Bias Voltage Influence on Linearity Performance of the ET and AET 10 W GaN HEMT PAs</u>	205
3.15.	Milicevic Milenko, Milinkovic Branislava, Radic Jelena, Djuric Radivoje <u>Digitally controlled attenuator</u>	208

3.16.	Parshin Yury, Kaznacheev Pavel <u>Testing of Channel Coefficients of MIMO Communication System in the Presence of Interferences Complex</u>	211
3.17.	Parshin Yuri, Kharin Aleksei, Kudryashov Vladislav <u>Wiener Filtering of Channel Coefficients in MIMO Communication System with Mobile Objects</u>	215
3.18.	Misailović Srđan, Marković Goran <u>Multihop hijerarhijski protokol rutiranja za bežične senzorske mreže većih dimenzija u uslovima neidealne konektivnosti</u>	218
3.19.	Pajkić Tijana, Čabarkapa Milan, Nešković Nataša, Budimir Đurađ <u>Uporedna analiza karakteristika 4G i 5G signala na fizičkom sloju</u>	222
3.20.	Marković Goran <u>Centralizovana dvostepena klasifikacija signala po tipu modulacije primenom mreže senzora</u>	226
3.21.	Vukotić Selen, Vučić Desimir <u>Združena detekcija i klasifikacija OFDM/QAM, OFDM/OQAM i SC-FDMA signala na bazi cikličnih obeležja</u>	230
3.22.	Obradović Danilo, Marković Goran, Pokrajac Ivan <u>Analiza mogućnosti primene morfoloških operacija u procesu segmentacije spektra za potrebe radio-goniometrije</u>	234
3.23.	Lukić Đorđe, Koprivica Mladen, Nešković Nataša, Nešković Aleksandar <u>Eksperimentalna analiza performansi 2G/3G/4G javne mobilne mreže</u>	238
3.24.	Petrović Slavko, Marković Čedomir, Šunjevarić Milan <u>Upravljanje nulama dijagrama usmjerenosti zračenja antenskog niza u uslovima elektronskog ometanja</u>	242

СЕКЦИЈА 4: СИСТЕМИ ПРЕНОСА СИГНАЛА (СПС) **SECTION 4: COMMUNICATIONS SYSTEMS (CS)**

4.1.	Amaricai Alexandru, Boncalo Oana <u>Cost effective FPGA implementation for Hard Decision LDPC decoders</u>	246
4.2.	Broulim Jan, Davarzani Sima, Georgiev Vjaceslav, Zich Jan <u>Genetic Optimization of a Short Block Length LDPC Code Accelerated by Distributed Algorithms</u>	250
4.3.	Likhobabin Evgeny, Vityazev Vladimir <u>Error Floor Reduction for LDPC Codes Using Self-Correction Technique</u>	254
4.4.	Dudáček Luděk, Veřtát Ivo <u>Multidimensional Parity Check codes with short block lengths</u>	258
4.5.	Solar-Nikolić Dino, Trifunović Marija, Milićević Predrag, Milosav Predrag, Perić Miroslav <u>Methodology for Used Cryptographic Key Verification in Multi Gbit/s Encryption Systems</u>	262

4.6.	Prokhorov Mikhail D., Kulminskiy Danil D., Ponomarenko Vladimir I. <u>Secure Communication Schemes using Chaotic Synchronization of Time-Delay Systems</u>	266
4.7.	Gecan Dragan, Olavsbraten Morten, Gjertsen Karl Martin <u>Comprehensive Investigation of a Dynamic Gate Biasing Technique for Linearity Improvement Based on Measurement of a 10 W GaN HEMT Power Amplifier</u>	270
4.8.	Matlak Stefan, Bogdan Razvan <u>Reducing Energy Consumption in Home Automation based on STM32F407 Microcontroller</u>	273
4.9.	Prlinčević Bojan, Spalević Petar <u>Performances of the image transmission over FSO channel in the last-mile connection (Invited paper)</u>	277
4.10.	Babaei Mohammadreza, Basar Ertugrul, Aygölü Ümit <u>A Cooperative Spectrum Sharing Protocol Using STBC-SM at Secondary User</u>	284
4.11.	Eslami Amir, Karamzadeh Saeid <u>Double Threshold Maximum to Minimum Eigen-value Spectrum Sensing Proposal and Performance Analysis</u>	288
4.12.	Javadi Sepideh, Soleimani-Nasab Ehsan <u>Two-Way Interference-Limited AF Relaying with Wireless Power Transfer</u>	292
4.13.	Khan Rida, Altunbas Ibrahim, Karabulut Kurt Gunes <u>Enhanced XOR Network Coding Scheme with OFDM for Two-Way Relay Networks</u>	296
4.14.	Karataş Azad, Özbek Berna, Sönmez İlker, Bengür Sıdıka <u>Load Based Cell Selection Algorithm for Tetra based Professional Mobile Radio</u>	300
4.15.	Mihajlović Živorad, Milankov Aleksandar, Živković Lazar, Tolić Miloš <u>Implementation of Wireless M-Bus Concentrator/Gateway for Remote Reading of Smart Gas Meters</u>	304
4.16.	Petković Milica, Đorđević Goran, Ivaniš Predrag <u>Partial relay selection with variable gain relays and outdated CSI in mixed RF/FSO system</u>	308
4.17.	Čiča Zoran <u>Pipelined Implementation of Camellia Encryption Algorithm</u>	312
4.18.	Vojvodić Hrvoje, Mandžuka Sadko, Vujić Miroslav <u>The Use of Cooperative Intelligent Transport Systems for Vehicle Emission Charging</u>	316
4.19.	Kljaić Zdenko, Lučić Andrea, Vidović Krešimir <u>Benefits of Utilisation of GPS Error Mitigation Models for Intelligent Transport Systems</u>	320
4.20.	Perra Cristian <u>A framework for the development of sustainable urban mobility applications</u>	324
4.21.	Burian Petr <u>Communication Stack for Generic Embedded Modular Control System</u>	328

4.22.	Sarb Daniel, Bogdan Razvan	
	<u>Wireless Motor Control in Automotive Industry</u>	332
4.23.	Peraković Dragan, Periša Marko, Cvitić Ivan, Husnjak Siniša	
	<u>Artificial Neuron Network Implementation in Detection and Classification of DDoS Traffic</u>	336
4.24.	Sandić Miladin, Bilbija Aleksandar, Velikić Ivan	
	<u>Analiza integracije različitih klasa saobraćaja u mixed-critical mrežama</u>	340

СЕКЦИЈА 5: ОБРАДА СИГНАЛА (OC) **SECTION 5: SIGNAL PROCESSING (SP)**

5.1.	Krmar Jelena, Papić Veljko	
	<u>Adaptation of integrodifferential operator using statistical characteristics of the iris texture</u>	344
5.2.	Brajović Miloš, Orović Irena, Stanković Srdjan	
	<u>The Optimization of the Hermite transform: Application Perspectives and 2D Generalization</u>	348
5.3.	Sokolov Rostislav I., Abdullin Renat R.	
	<u>Nonlinear JPEG Image Filtering at Stage of Preprocessing</u>	352
5.4.	Murgia Francesca, Giusto Daniele	
	<u>A Comparison of Raw Light Field Lossless Data Compression Algorithms</u>	356
5.5.	Živić Nataša, Ur-Rehman Obaid	
	<u>Soft Authentication Techniques for Images</u>	360
5.6.	Sumalan Andrada, Popescu Dan, Ichim Loretta	
	<u>Flooded area detection using UAV images</u>	364
5.8.	Stojanovic Branka, Marques Oge, Nešković Aleksandar, Puzović Snezana	
	<u>Fingerprint ROI Segmentation Based on Deep Learning</u>	368
5.9.	Bobić Vladislava, Graovac Stevica	
	<u>Development, implementation and evaluation of new eye tracking methodology</u>	372
5.10.	Starčević Đorđe, Ostojić Vladimir, Petrović Vladimir	
	<u>Automatic Evaluation of Collimation Field Rotation Angle in Digital Radiographic Images</u>	376
5.11.	Starčević Đorđe, Ostojić Vladimir, Petrović Vladimir	
	<u>An Open-source Digital Diagnostic Radiography Image Annotation Software</u>	380
5.12.	Ostojić Vladimir, Starčević Đorđe, Petrović Vladimir	
	<u>Low-value Laplacian Pyramid Coefficient Prediction for Faster Radiography Image Processing</u>	384
5.13.	Vujković Milorad, Rajović Vladimir, Jovičić Nenad	
	<u>Ten axis MEMS as a position sensor</u>	388
5.14.	Škobić Velibor, Marić Uroš, Tomić Stefan, Rešetar Ivan	
	<u>Real Time Video Freezing Detection Implementation on FPGA</u>	392

5.15.	Bilibajkić Ružica <u>Recognition of articulatory-acoustic deviations in pathological speech (Invited paper)</u>	395
5.16.	Delić Tijana, Sečujski Milan <u>Sinteza govora na srpskom jeziku zasnovana na veštačkim neuralnim mrežama</u>	403
5.17.	Gusev Marjan, Ristovski Aleksandar, Guseva Ana <u>Distribution of premature heartbeats</u>	407
5.18.	Tzallas Alexandros, Tsoulos Ioannis, Tsipouras Markos, Giannakeas Nikolaos, Androulidakis Iosif, Zaitseva Elena <u>Classification of EEG signals using feature creation produced by grammatical evolution</u>	411
5.19.	Kaprocki Nives, Kovačević Jelena, Pečkai Kovač Robert, Malko Miroslav <u>DSP implementation of the surround speaker virtualizer for sound bar systems</u>	415
5.20.	Jakovljević Nikša, Mišković Dragiša, Trpovski Željko <u>Evaluation of Noise Estimation Algorithms Based on Minimum Statistics and Signal to Noise Ratio</u>	419
5.21.	Radosavljević Zvonko, Kovačević Branko <u>Integrated Track Splitting-Component Merging History for Efficient Target Tracking (Invited presentation)</u>	423
5.22.	Deiaa Ahmed, Abdelhamid Bassant, El-Keyi Amr, Elramly Salwa <u>Mitigation of Both Doubly Selective Channels and Phase Noise Effects in OFDM Systems</u>	427
5.23.	Samano Robles Ramiro, Tovar Eduardo, Lara Mauricio, Orozco Aldo, McLernon Desmond C., Ghogho Mounir <u>Network Diversity Multiple Access in Rayleigh Fading Correlated Channels with Imperfect Channel and Collision Multiplicity Estimation</u>	431
5.24.	Refan Mohammad Hossein, Dameshghi Adel, Kamarzarrin Mehrnoosh <u>GDOP Factor Classification by Implementation of CMACNN Method for Low Cost GPS Receivers</u>	435
5.25.	Stanciu Cristian, Udrea Mihnea, Anghel Cristian, Dobre Robert Alexandru <u>Improved Regularization for a Low-Complexity RLS Algorithm</u>	439
5.26.	Al-Zubaidi R-Smith Nawfal, Brancik Lubomir <u>Convergence Acceleration Techniques for Proposed Numerical Inverse Laplace Transform Method</u>	443
5.27.	Nikishkin Paul B., Vityazev Sergey V., Subbotin Igor V., Kharin Aleksei V., Vityazev Vladimir V. <u>Sub-band OFDM Implementation on Multicore DSP</u>	447
5.28.	Bujaković Dimitrije, Đurović Željko, Andrić Milenko, Bondžulić Boban, Simić Slobodan <u>Expert System Based on Hidden Markov Models for Recognition of Radar Targets (Invited paper)</u>	451

5.29.	Stojković Nikola, Nikolić Dejan, Džolić Bojan, Tošić Nikola, Orlić Vladimir, Lekić Nikola, Todorović Branislav <u>An Implementation of Tracking Algorithm for Over-The-Horizon Surface Wave Radar</u>	459
5.30.	Nikolić Veljko, Marković Vera V. <u>Determining the DOA of Received Signal Using RBFNN Trained with Simulated Data</u>	463
5.31.	Tomic Slavisa, Beko Marko, Dinis Rui, Tuba Milan, Bacanin Nebojsa <u>An Efficient WLS Estimator for Target Localization in Wireless Sensor Networks</u>	467
5.32.	Brajić Miloš, Draganić Andjela, Orović Irena, Stanković Srdjan <u>FHSS signal sparsification in the Hermite transform domain</u>	471
5.33.	Djukanović Slobodan, Popović Tomo, Mitrović Andrija <u>Precise sinusoid frequency estimation based on parabolic interpolation</u>	475
5.34.	Daković Miloš, Stanković Isidora, Ender Joachim, Stanković Ljubiša <u>Sample Selection Strategy in DFT based Compressive Sensing</u>	479

СЕКЦИЈА 6: ПРИМЕЊЕНА ЕЛЕКТРОМАГНЕТИКА (ПЕМ) **SECTION 6: APPLIED ELECTROMAGNETICS (AEM)**

6.1.	Aşci Yavuz, Pehlivan Mustafa, Yeğın Korkut <u>Wideband, High Gain Aperture Coupled Ku-Band Antenna for SatCom</u>	483
6.3.	Ahmadfard Farnaz, Razavi Seyed Ali <u>Backlobe Suppressed H-plane Ridge Gap Waveguide (RGW) Horn Antenna</u>	486
6.4.	Subotic Milos, Palfi Laszlo, Pjevalica Nebojsa <u>Efficient Antenna Modeling Method for Microwave Tomography</u>	490
6.5.	Yiğit Olcay, Şişman İsmail, Aşci Yavuz, Yeğın Korkut <u>Beam Switched Antenna Design for Jamming Mitigation of GPS Receivers</u>	494
6.6.	Lazović Luka, Jovanović Ana, Lutovac Budimir, Rubežić Vesna <u>Primjena teorije grafova za dizajniranje rekonfigurabilnih fraktalnih antena</u>	497
6.7.	Milošević Vojislav, Jakanović Branka, Boric-Lubecke Olga, Lubecke Victor <u>Key Millimeter Wave Technologies for 5G (Invited presentation)</u>	501
6.9.	Artner Gerald, Langwieser Robert, Mecklenbräuker Christoph <u>A MID Dipole Antenna in LDS Technology</u>	502
6.10.	Pehlivan Mustafa, Yeğın Korkut, Aşci Yavuz <u>Design of 1-18 GHz Parabolic Reflector Antenna with LPDA Feed</u>	506
6.11.	Sarbazi Golazari Samineh, Amiri Nasrin, Hojjat Kashani Farrokh <u>Design, Simulation and Measurement of Loop Plasma Antenna in UHF Band</u>	509
6.13.	Zouros Grigorios <u>Alternative Orthogonal Vector Basis Functions for Solving Volume Integral Equations in Electromagnetic Theory (Invited paper)</u>	513
6.14.	Perovic Jovana, Olcan Dragan <u>The Excess Attenuation of Electric Field in the Presence of Deciduous Trees</u>	519

6.15.	Kolezas Georgios, Zouros Grigorios <u>Electromagnetic Scattering by a Conducting Sphere with Anisotropic Coating</u>	523
6.16.	Pjevalica Velibor, Pjevalica Nebojsa, Petrovic Nenad, Teslic Nikola <u>A Novel Simulation Approach for Measuring of DC Voltage Offset Influence on Magnetizing Current Even Harmonics in the Power Transformer by Using Dither</u>	527
6.17.	Plavec Eduard, Vidović Mladen <u>Genetic Algorithm Based Plunger Shape Optimization of DC Solenoid Electromagnetic Actuator</u>	531
6.18.	Savić Slobodan, Ilić Milan <u>Curved Continuously Inhomogeneous and Anisotropic Higher Order Finite Elements for Accurate Large-Domain Electromagnetic Modeling (Invited paper)</u>	535
6.19.	Miletić Milivoje F. <u>Comparison of Four MoM Formulations When Solving Electrostatic Problems Where Rotational Symmetry Is Exploited</u>	543
6.20.	Ozturk Hulya, Yegin Korkut <u>Electromagnetic Scattering from PEC Wedge in Lossy Medium due to Point Current Source</u>	547
6.21.	Antić Danko, Mijatović Gorana, Djuric Nikola, Mazzarella Giuseppe <u>The COMSOL Support for Analysis of Inductors with Variable Geometry</u>	550
6.22.	Kozhevnikov Vasily Yu., Kozyrev Andrey V., Semeniuk Natalia S. <u>Kinetic Modelling of the One-dimensional Planar Virtual Cathode Oscillator</u>	554
6.23.	Bjelić Slobodan, Marković Nenad, Bjelić Igor, Bogičević Zorica, Jakšić Uroš, Jovanović Bojan <u>Detektovanje inverzne komponente struje pomoću projektovanog filtra struje</u>	558
6.24.	Semenikhina Diana V., Chikov Nikolay I., Semenikhin Andrey I., Gorbatenko Nikolay N. <u>Experimental Studies of Nonlinear Metasurface with Metamaterial Substrate</u>	562
6.25.	Crnadak Veljko, Tasić Siniša <u>Improved VHF Quadrature Hybrid Coupler</u>	566
6.26.	Yukhanov Yuriy V., Privalova Tatiana Yu. <u>Synthesis of Impedance Reflection Type Polarizer</u>	570
6.27.	Djuric Nikola, Kljajic Dragan <u>An Approach for the Design of Chemical Reactors under Microwave Irradiation</u>	574
6.28.	Letavin Denis A., Mitelman Yuriy E., Chechetkin Victor A. <u>The Substrate Factor in the Miniaturization Efficiency of the Microstrip Branch-Line Couplers</u>	577
6.29.	Djuric Nikola, Bjelica Jelena <u>Resonant Cavity as Microwave Exposure System for Biological Materials</u>	581
6.30.	Blagojevic Marjan, Mancic Dragan <u>The system for magnetic field mapping based on the integrated 3D Hall sensor (Invited paper)</u>	585

6.31.	Kundacina Ognjen, Vincan Vladimir, Antic Danka, Kljajic Dragan, Kasas-Lazetic Karolina <u>The ABM 8057 Utilization for SEMONT's EMF Monitoring in a Closed Room</u>	593
6.32.	Bjelica Jelena, Mijatovic Gorana, Kljajic Dragan, Fanti Alessandro <u>The Inductors with Adjustable Surface Area for Energy Harvesting Utilization</u>	597
6.33.	Nešić Nataša, Dončov Nebojša, Russer Johannes, Milovanović Bratislav <u>Electromagnetic Field Coupling with Two Monopole Antennas inside a Protective Enclosure with Apertures</u>	601
6.34.	Prucevs Andrejs, Semenjako Janis, Solovjova Tatjana <u>New Shape Induction Loop for Small and Medium-Size Rooms</u>	605
6.35.	Nedic Goran, Djuric Nikola, Kljajic Dragan, Milutinov Miodrag, Josic Slobodan <u>The Exposure Assessment to Communication Equipment of High-power Substation</u>	609

СЕКЦИЈА 7: ПРИМЕЊЕНА ЕЛЕКТРОНИКА (ПЕЈ)

SECTION 7: APPLIED ELECTRONICS (AEL)

7.1.	Stenin Vladimir Ya., Antonyuk Artem V. <u>Design of the 65-nm CMOS Comparison Element for a Content-Addressable Memory and Simulation of Single-Event Transients</u>	613
7.2.	Katunin Yuri V., Stenin Vladimir Ya., Antonyuk Artem V. <u>Design of Logical Elements with Single-Event Compensation for the 28-nm CMOS Decoders</u>	617
7.3.	Rakanović Damjan, Struharik Rastislav <u>IP Core for AES256 and TDES algorithms with AXI Interface</u>	621
7.4.	Ćatić Vladimir, Lukić Natalija, Salom Iva, Ristić Vukasin, Kabović Milenko, Nenadić Nikola <u>An Automated Environment for Hardware Testing Using PXI Instrumentation and LabVIEW Software</u>	625
7.5.	Moldaschl Jan, Darwish Mohamed <u>An Ultra Flat Profile Natural Interleaved Power Factor Corrector with Advanced Power Management</u>	629
7.6.	Shetty Darshan, Pasupureddi Vijaya, Sturm Johannes <u>A 2.4 GHz, 1 dB Noise Figure Common-Gate LNA for WLAN Frontend</u>	633
7.7.	Olyae Saeed, Sherafat Masood <u>Investigation of target speed effect on nonlinearity error caused by non-ideal PBS in DTLMI</u>	637
7.9.	Rajs Vladimir, Mihajlović Živorad, Milosavljević Vladimir, Tomić Josif, Živanov Miloš <u>Methods of environmental monitoring parameters based on smart measurement systems (Invited presentation)</u>	641
7.10.	Zich Jan, Broulim Jan <u>Wireless Unit for Environmental Monitoring</u>	643

7.11.	Savić Trifun, Radonjić Milutin <u>Proposal of Solution for Automated Irrigation System</u>	647
7.12.	Lufinka Ondřej <u>Multiple-Point Ultrasonic Distance Measurement and Communication with Simulations</u>	651
7.13.	Milovanović Marko, Popović Ivan T., Rakić Aleksandar Ž. <u>Run-Time Reconfigurable Middleware in Device Network Architecture</u>	655
7.14.	Bundalo Zlatko, Ramakić Adnan, Bundalo Dušanka <u>Increasing Desktop Application and User Data Protection Using Smartphone</u>	659
7.15.	Mandzuka Sadko, Gregurić Martin, Kljaić Zdenko <u>Kooperativno okruženje za infrastrukturu e-mobilnosti</u>	663

СЕКЦИЈА 8: МУЛТИМЕДИЈА (MM) **SECTION 8: MULTIMEDIA (MM)**

8.1.	Şekertekin Yeter, Atan Özkan <u>An Image Encryption Algorithm Using Ikeda and Henon Chaotic Maps</u>	667
8.2.	Perra Cristian <u>On the Quality Evaluation of Lossy Compressed Light Fields</u>	671
8.3.	Murgia Francesca, Giusto Daniele <u>Low Cost Plenoptic Camera for 3D Modelling</u>	675
8.4.	Tuba Ira, Tuba Eva, Beko Marko <u>Digital Image Forgery Detection Based on Shadow Texture Features</u>	679
8.5.	Ahmadvand Pouya, Ebrahimpour Reza, Ahmadvand Payam <u>How popular CNNs Perform in Real Applications of Face Recognition</u>	683
8.6.	Stojak Aleksandar, Tuba Eva, Tuba Milan <u>Framework for Abnormality Detection in Magnetic Resonance Brain Images</u>	687
8.7.	Nikolic Marina, Tuba Eva, Tuba Milan <u>Edge Detection in Medical Ultrasound Images Using Adjusted Canny Edge Detection Algorithm</u>	691
8.8.	Mašović Draško <u>Towards the Improved Prediction of Airborne Sound Insulation in Buildings (Invited paper)</u>	695
8.9.	Ristić Dragan <u>Karakterizacija tekstone impulsnog odziva prostorijske primenom multifraktalne analize (Invited paper)</u>	703
8.10.	Salom Iva <u>Analiza akustičkih karakteristika zvuka zvana (Invited paper)</u>	711
8.11.	Petrović Nikola, Bjelić Miloš, Čertić Jelena, Stanojević Miodrag, Šumarac Pavlović Dragana <u>Analyses of decimation filter stopband attenuation influence on subjective quality of audio signals</u>	719

8.12.	Dinić Miodrag, Rikalo Aleksandar, Marković Aleksandar, Jovanović Petar <u>Optimizacija održavanja asocijativnog bafera jedinice za upravljanje memorijom u Android emulatoru za arhitekturu MIPS</u>	723
8.13.	Zekovic Amela <u>Multifractal and Inverse Multifractal Characterization of 3D Video Formats (Invited paper)</u>	727
8.14.	Marković Dejan R., Gavrovska Ana, Reljin Irini <u>4K Video Traffic Analysis using Seasonal Autoregressive Model for Traffic Prediction</u>	735
8.15.	Popadić Ilija, Todorović Branislav <u>Image fusion based on the multi-exposed images</u>	739
8.16.	Fimic Nemanja, Basicevic Ilija <u>Software architecture for supporting FCC in DVB-S/C/T set top boxes</u>	743
8.17.	Srejić Tijana, Ilić Miloš, Milošević Milena <u>Realizacija mehanizma za isporuku događaja u aplikativnom okruženju vozila</u>	747
8.18.	Šoškić Nenad, Benarik Laslo, Bašičević Ilija <u>Jedno rešenje propagiranja EPG podataka na digitalnom televizijskom prijemniku</u>	751
8.19.	Đekić Nemanja, Janković Miloš, Pjevalica Nebojša <u>USB-om kontrolisan relejni prekidač</u>	754

СЕКЦИЈА 9: СОФТВЕРСКИ АЛАТИ И АПЛИКАЦИЈЕ (СAA) **SECTION 9: SOFTWARE TOOLS AND APPLICATIONS (STA)**

9.1.	Simonović Mirela, Živojnović Vojin, Saranovac Lazar <u>An Approach to Modeling Clock Tree of a Complex System-on-Chip</u>	758
9.2.	Budyakov Petr S., Chernov Nikolay I., Yugai Vladislav Ya., Prokopenko Nikolay N. <u>Logic Functions Representation and Synthesis of k-valued Digital Circuits in Linear Algebra</u>	762
9.3.	Petrosyants Konstantin, Kharitonov Igor, Batarueva Ekaterina <u>Simulation of CMOS IC's Waveforms Distortions in PCB Traces with Account for Radiation Effects</u>	766
9.4.	Živković Miodrag, Jovanović Nemanja <u>NFC TraceCase</u>	770
9.5.	Pesic Djordje, Djordjevic Borislav, Timcenko Valentina <u>Competition of Virtualized Ext4, Xfs and Btrfs Filesystems Under Type-2 Hypervisor</u>	774
9.6.	Janković Novica, Barjaktarović Marko, Janković Milica, Čantrak Djordje <u>First steps in new affordable PIV measurements</u>	778
9.7.	Marković Ivo, Nešović Marko, Davidović Mile <u>Jedno rešenje testiranja set-top boks hardvera na fabričkoj liniji</u>	782

9.8.	Murgia Francesca, Giusto Daniele <u>A Database for Evaluating the Quality of Experience in Light Field Applications</u>	786
9.9.	Hadžić Đulaga, Sarajlić Nermin, Malkić Jasmin <u>Different Similarity Measures to Identify Duplicate Records in Relational Databases</u>	790
9.10.	Spalević Petar, Pećanin Emir, Ilić Siniša, Veinović Mladen, Panić Stefan <u>Automatization of the ETL process on the isolated small scale database systems</u>	794
9.11.	Nenadić Kosa, Gavrić Milan <u>Enhancing CIM with Linked Data Capability</u>	798
9.12.	Lakshen Guma, Vraneš Sanja, Janev Valentina <u>Big Data and Quality: A Literature Review</u>	802
9.13.	Petkovski Aleksandar, Risteska Stojkoska Biljana, Trivodaliev Kire, Kalajdziski Slobodan <u>Analysis of Churn Prediction: A Case Study on Telecommunication Services in Macedonia</u>	806
9.14.	Guseva Ana, Gusev Marjan, Chorbev Ivan <u>iKnow Usability: A Case Study</u>	810
9.15.	Skorput Pero, Vujic M., Bokulic D. <u>Upravljanje znanjem u domeni inteligentnih transportnih sustava</u>	814
9.16.	Szabo Roland, Gontean Aurel <u>Lynxmotion AL5 Type Robotic Arm Control with Color Detection on FPGA Running Linux OS</u>	818
9.17.	Simion Catalin Eugen, Geampalia Giorgiana, Chenaru Oana Gabriela, Florea Gheorghe <u>Solutions for the Implementation of Remote Plant Supervision</u>	822
9.18.	Simić Aleksandra, Kocić Ognjen, Bjelica Milan Z., Milošević Milena <u>Driver monitoring algorithm for Advanced Driver Assistance Systems</u>	826
9.19.	Todorovici Lucian, Bogdan Razvan <u>Studying Thermal Behavior of Buildings and Vehicles by Means of a Mobile Thermographic System</u>	830
9.20.	Kocić Ognjen, Simić Aleksandra, Bjelica Milan Z., Maruna Tomislav <u>Optimization of driver monitoring ADAS algorithm for heterogeneous platform</u>	834
9.21.	Tuponja Boris, Šuković Goran <u>Evolving Neural Network to Play Game 2048</u>	838
9.22.	Tsoulos Ioannis, Tzallas Alexandros, Tsalikakis Dimitrios, Giannakeas Nikolaos, Tsipouras Markos, Androulidakis Iosif, Zaitseva Elena <u>NeuralGenesis: a software for distributed neural network training</u>	841
9.23.	Punt Marija <u>Human-computer interaction in an integrated environment combining digital TV, mobile devices and Internet (Invited paper)</u>	845
9.24.	Krnjetin Marko, Četić Nenad <u>Scripting framework for graphical configuration tool for embedded devices</u>	853

9.25.	Žitnik Slavko, Janković Marko, Petrovčič Klemen, Bajec Marko <u>Architecture of Standard-based, Interoperable and Extensible IoT Platform</u>	857
9.26.	Šandi Stevan, Krstajić Božo, Popović Tomo <u>pyPMU – Open Source Python Package for Synchrophasor Data Transfer</u>	861
9.27.	Rajinac Marko, Krunić Momčilo, Považan Ivan, Kovačević Jelena <u>An Eclipse plugin for preserving and reloading debug session, in domain of DSP application development</u>	865
9.28.	Žica Petar, Vranić Nikola <u>Jedno rešenje programske sprege ulaznih uređaja i Android platforme sa primerom integracije Nunchuk uređaja</u>	869
9.29.	Bosnic Stefan, Papp Istvan, Novak Sebastian <u>The development of hybrid mobile applications with Apache Cordova</u>	873
9.30.	Vukov Goran, Kovačević Marko, Kovačević Branimir, Maruna Tomislav <u>Jedna realizacija čuvanja podataka o događajima u vozilu u Androidu operativnom sistemu</u>	877
9.31.	Drašković Dražen, Gencel Vidor, Žitnik Slavko, Bajec Marko, Nikolic Bosko <u>A software agent for social networks using natural language processing techniques</u>	881
9.32.	Delčev Sanja, Vukasović Maja, Drašković Dražen, Radojević Dimitrije, Janković Marko, Bajec Marko, Nikolić Boško <u>Veb sistem za polaganje testova znanja iz Inteligentnih sistema</u>	885
9.34.	Batanović Vuk, Nikolić Boško <u>Sentiment Classification of Documents in Serbian: The Effects of Morphological Normalization</u>	889
9.35.	Pijetlović Stefan, Jovanović Petar, Teslić Nikola, Prljević Slobodan <u>An Approach to Software Development Tools Unification</u>	893
9.36.	Šimić Goran, Pantić Stefan, Jevremović Aleksandar <u>Framework for Improving Web Based e-Learning Interactivity</u>	897
9.37.	Vukovic Dijana, Dujlovic Igor <u>Facebook Messenger bots and their application for business</u>	901
9.38.	Nešić Zoran, Ljubić Leon, Radojičić Miroslav, Vesić Vasović Jasmina <u>Some Considerations on Improvement of Parking Service Information System</u>	905
9.39.	Lutovac Miroslav, Mladenović Vladimir, Lutovac-Banduka Maja <u>Graphical User Interface for Electrical Engineering Systems using Wolfram Language</u>	909
9.40.	Velkoski Goran, Gusev Marjan, Ristov Sasko <u>Analysis of Interactive Image Technologies</u>	913
9.41.	Perra Cristian <u>Light Field Image Compression Based on Preprocessing and High Efficiency Coding</u>	917
9.42.	Mišić Marko, Nikolov Dušan, Protić Jelica, Tomašević Milo <u>Paralelizacija GST algoritama za detekciju sličnosti u programskom kodu</u>	921

9.43.	Ristovski Aleksandar, Gusev Marjan, Ristov Sasko <u>Nested Parallelism Concepts of Ray Tracing Algorithms and Multithreading API Performance Analysis</u>	925
9.44.	Boštjančič Rakas Slavica, Stojanović Mirjana <u>A Policy-Based Approach to E2E Service Negotiation via Third Party Agent</u>	929
9.45.	Bjelić Igor S., Bogićević Zorica S., Bjelić Slobodan N., Marković Nenad A., Jovanović Bojan L. <u>Simulation of daylight in order to protect the sustainability of the old architectural structures cultural heritage in the environment</u>	933
9.46.	Đorđević Nebojša D., Rančić Dejan D., Spalević Petar <u>Development of a Methodology for Evaluating the Quality in Use of WebGIS applications (Invited presentation)</u>	937

СЕКЦИЈА 10: ОПТИЧКЕ ТЕЛЕКОМУНИКАЦИЈЕ (ОТ) **SECTION 10: OPTICAL COMMUNICATIONS (OC)**

10.1.	Krstić Marko, Crnjanski Jasna, Totović Angelina, Gvozdić Dejan <u>Injection-Locked Fabry-Pérot Laser Diodes for All-Optical Flip-Flops (Invited paper)</u>	939
10.2.	Stojanovic Nebojsa, Karinou Fotini, Prodaniuc Cristian, Qiang Zhang <u>Timing Recovery in High-Speed PAM-4 Transmission Systems</u>	947
10.3.	Uzunidis Dimitris, Matrakidis Chris, Stavdas Alexandros <u>Simplified FWM Model for Flexible-Grid Optical Systems</u>	951
10.4.	Uzunidis Dimitris, Matrakidis Chris, Stavdas Alexandros <u>Application of a Simplified FWM Expression in Mixed-Fiber Links</u>	955
10.5.	Ashrafzadeh Behnam, Soleimani-Nasab Ehsan, Kamandar Mehdi <u>Outage Analysis of Mixed RF/FSO Cooperative Systems with Multiple Antennas and Apertures</u>	959
10.6.	Ashrafzadeh Behnam, Soleimani-Nasab Ehsan, Kamandar Mehdi <u>Performance Analysis of Mixed DGG and Generalized Nakagami-m Dual-Hop FSO/RF Transmission Systems</u>	963
10.7.	Ratkoceri Jakup, Samir Ahmed, Batagelj Boštjan <u>Seven-Core Optical Fiber in a Passive Optical Access Network</u>	967
10.8.	Miljković Nemanja, Stojanović Aleksandar, Matavulj Petar <u>Physical model for B92-QKD authentication based on analogy with optical chaotic systems</u>	971

СЕКЦИЈА 11: СТУДЕНТСКА СЕКЦИЈА (С)
SECTION 11: STUDENT PAPERS (S)

11.1.	Bogdanović Ana, Gajić Andrija <u>Labview aplikacija za upravljanje Lego Mindstorms robotom pomoću Microsoft Kinecta</u>	975
11.2.	Gligorovski Vojislav, Mitrović Božidar, Milovanović Marta <u>Upravljanje Lego Mindstorms robotom pomoću Leap motion senzora</u>	979
11.3.	Stojanović Nenad <u>Uticaj Rajsovog fedinga na kvalitet slike pri prenosu u okviru WMSN</u>	983
11.4.	Lazić Aleksandar <u>Modelovanje RPO-OFDM modulatora u VLC sistemima</u>	987
11.5.	Janković Nada <u>Razvoj sistema za elektronsko glasanje i implementacija korišćenjem Laravel tehnologije</u>	991
11.6.	Čakarević Magdalena <u>Analysis and calculation of user's location in indoor and outdoor environment</u>	995
11.7.	Matic Milena <u>Practical implementation of location services in Bluetooth Low Energy systems</u>	999
11.8.	Letavin Denis A. <u>Compact double-frequency microstrip coupler</u>	1003
11.9.	Letavin Denis A. <u>Miniature band-pass filter on microstrip resonators</u>	1006
11.10.	Ćapin Miloš <u>Mikrotalasni rekonfigurabilni filter propusnik niskih učestanosti ili opsega učestanosti</u>	1009
11.11.	Marković Ivo <u>Implementacija rekonfigurabilnog filtra propusnika – nepropusnika opsega učestanosti korišćenjem memristora</u>	1013

Simulation of CMOS IC's Waveforms Distortions in PCB Traces with Account for Radiation Effects

Konstantin O. Petrosyants, Igor A. Kharitonov, Ekaterina I. Batarueva

Abstract — It is shown that it is necessary to take into account the radiation influence on CMOS IC's characteristics in the process of waveforms distortions analysis in satellite communication PCB traces. The special SPICE models for CMOS transistors with radiation dependent model parameters were used for this purpose. The simulation results showed that total dose results to reducing of IC output current, increasing of IC output voltage rise/fall times and to reduction of parasitic oscillations in PCB traces. The influence of irradiated P- channel MOSFET on transient characteristics is more important than N-MOSFET's influence because of summarized mobility reduction and threshold voltage value increasing.

Keywords — CMOS, integrated circuit, printed circuit board, board trace, signal waveform, signal integrity, SPICE model, IBIS model, space telecommunication systems, radiation effects.

I. INTRODUCTION

Digital systems, realized on printed circuit boards (PCBs) are the main parts of satellite communication systems. Digital electronic component properties (input, output impedances, output driving capability, signal slew rates, etc.) as well as parasitic parameter of components packages, PCB traces electrical parameters and signal parameters influence the signal integrity (SI) – i.e. signal waveform distortions and PCB traces inter influence [1-3] (see Fig. 1). Higher PCBs and ICs integration densities and their higher working frequencies result to more complicated design of high-speed telecommunication systems on modern PCBs.

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PCB designers and signal integrity engineers can use a variety of simulation tools to perform their design tasks [3,4]. SPICE simulation tools are used as the detailed circuit analysis tools. IBIS models of digital components are widely used for signal integrity analysis at PCB level [3, 7]. They are used for less accurate but faster SI analysis than SPICE simulation tools.

Many questions of SI analysis for commercial telecommunication components and PCBs are well described and discussed [1-3, 7]. However, telecommunication systems for satellite, aerospace and some other special applications work under the influence of space radiation (total dose, single events [4-6]) factors. Some aspects of radiation and temperature account in SI analysis with IBIS models were described by the authors [8]. However the questions of detailed SPICE simulation of signal integrity analysis in special digital systems on PCB under radiation influence were not described enough.

In this paper we consider some results of SPICE simulation of CMOS IC output waveforms distortions in PCB trace with account for radiation influence.

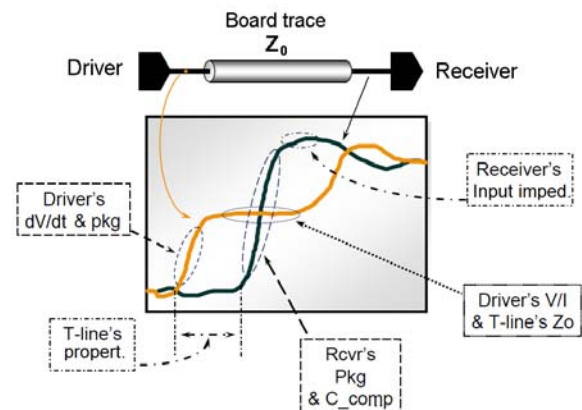


Fig. 1. Signal waveforms correlation with trace and driver/receiver parameters ([3]).

II. THE PROCEDURE OF SPICE SIMULATION OF CMOS IC WAVEFORMS DISTORTIONS IN PCB TRACES WITH ACCOUNT FOR RADIATION EFFECTS

When irradiated IC (Driver in Fig.1) sends its signal to receiving IC (Receiver in Fig.1) through complex resistance Z_0 of PCB trace the following factors influence the waveform distortions (see Fig. 1) [2,3]:

- R, L, C - parameters of PCB trace;
- input and output parameters of ICs which are radiation

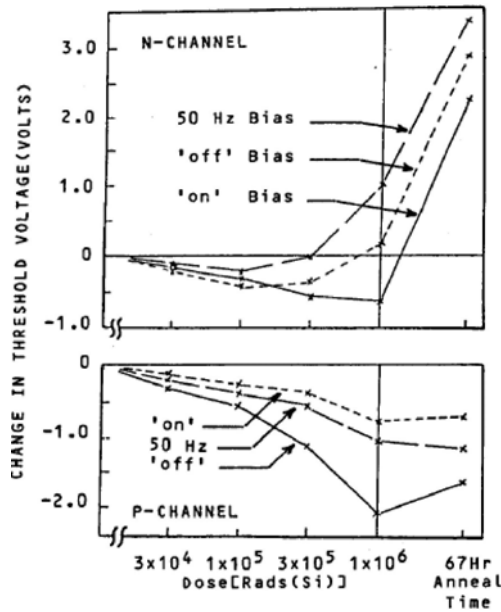
dependent.

R,L,C- parameters of PCB trace can be considered as radiation independent.

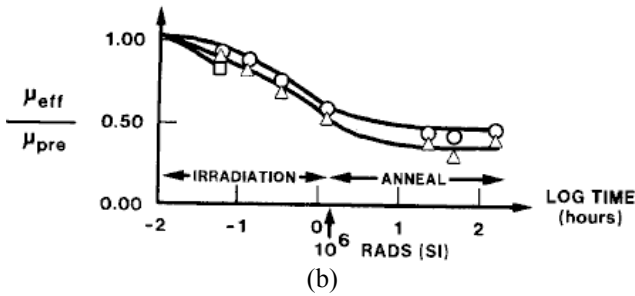
For CMOS IC the main radiation dependent output parameters and characteristics are: output stage transistors static (I-V-curves) and dynamic (output slew rate) characteristics. The changes in CMOS IC input impedance for IC (as a Receiver) can be neglected.

To account for radiation effects in MOSFET characteristics it necessary to consider [9-11]:

- MOSFETs threshold voltage V_{to} shifts (reduction of threshold voltage V_{ton} for N-MOSFET and growth of absolute value of V_{top} for P-MOSFET) and
- MOSFET mobility μ_{eff} reduction due to total dose of irradiation (Fig. 2).



(a)



(b)

Fig. 2. MOSFET threshold voltage shift (a) and mobility degradation (b) as a function of total dose and post-irradiation anneal [9].

All these factors result to reduction of IC output driving current and integrated circuit timing degradation (Fig. 3 from [9]). P- MOSFET influence on transient characteristics degradation (t_{plh}) can be more noticeable because of combined P- MOSFET mobility reduction and threshold voltage value increasing.

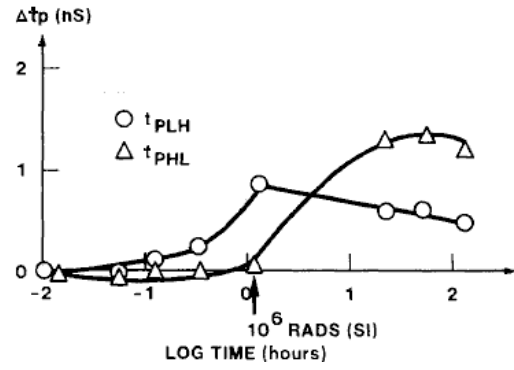


Fig. 3. Changes in integrated circuit timing as a function of total dose and post irradiation biased anneal [9].

In our previous works we have developed and successively used SPICE models for semiconductor electronic component, including CMOS FETs with account for different kinds of radiation [12, 13]. In this work we used these SPICE models for CMOS transistors of IC to realize SI analysis.

The circuit for SPICE simulation was the following (Fig. 4): the pair of output CMOS FETs (as a Driver), usual distributed RLC circuit for PCB trace and $1 \cdot 10^6$ Ohm resistance and 4pF capacitance (as a Receiver input impedance).

The procedure of SPICE simulation and analysis of signal waveform distortions in PCB trace with account for total dose effects was the following:

1. SPICE simulation of the equivalent circuit (Fig. 4) before irradiation and detailed analysis of signal waveforms distortions at the output stage node ("Driv_Out") and Receiver input node after PCB trace ("Receiv_Inp" in Fig. 4).
2. SPICE simulation of the equivalent circuit (Fig. 4) after a set of total dose and detailed analysis of signal waveforms distortions at the output stage node ("Driv_Out") and Receiver input node after trace ("Receiv_Inp" in Fig. 4). Identification of signal waveform distortions due to irradiation.

III. RESULTS OF SPICE SIMULATION OF CMOS IC WAVEFORMS DISTORTIONS IN PCB TRACES WITH ACCOUNT FOR RADIATION EFFECTS

As an example we used output transistor parameters for CMOS IC (KR1561IE21) designed for satellite digital systems:

- MOSFET channel length – 1.5 μm , width- 1300 μm (N-MOSFET), 1600 μm (P-MOSFET),
- gate oxide thickness – 30 nm,
- N-MOSFET threshold voltage $v_{to_n}=1.2\text{V}$,
- P-MOSFET threshold voltage $v_{to_p}= -1.3\text{V}$.

Transistors parameters degradation after 300krad [4,6] total dose:

- Threshold voltage for N_MOSFET $v_{to_n}= 0.5\text{V}$,
- Threshold voltage for P_MOSFET $v_{to_p}= - 2.4\text{V}$.

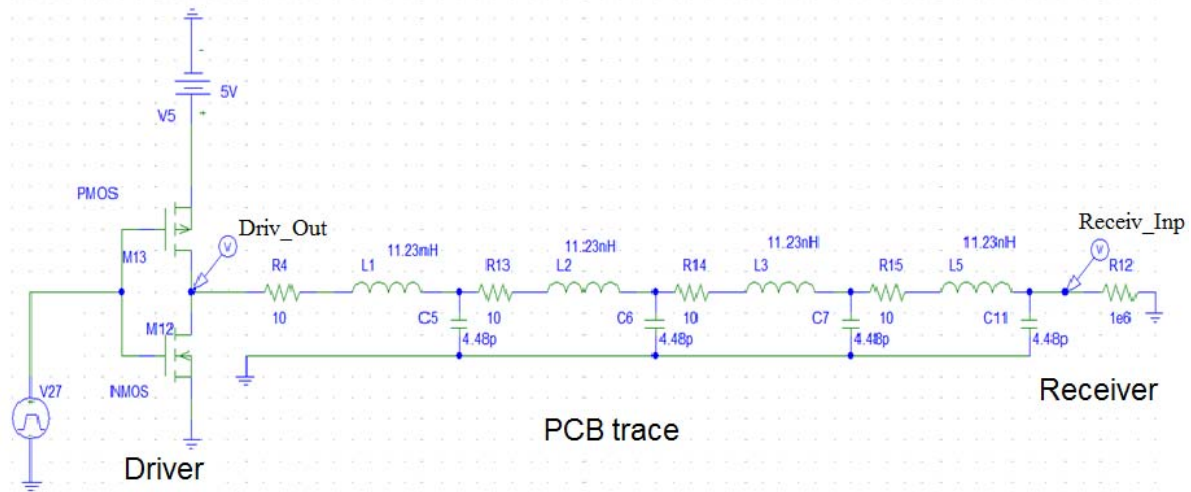


Fig. 4. Circuit for SPICE simulation of signal waveforms distortions.

PCB copper traces with the different parameters were simulated:

- trace length - 10...100mm,
- copper thickness - 18 and 36μm,
- trace width - 0.1...0.5 mm,
- PCB material - FR4, 4 layers.

Parasitic RLC-parameters for the traces were calculated using [1,3].

- Signal receiver input impedance - $R=10^6$ Ohm,
- $C=4 \cdot 10^{-12}$ F.

The results are presented for 100 mm trace. The equivalent circuit for SPICE simulation was presented in Fig. 4.

Fig.5 presents SPICE simulated output buffer signal waveform (solid line) and receiver input signal waveform (dashed line) after trace before (a) and after 100krad (b) and 300krad dose (c).

It is seen that the rising time t_{rise} becomes 8 ns after 300 krad in comparison with 2 ns for the circuit before irradiation. Falling time t_{fall} becomes 2.3 ns after 300 krad in comparison with 2 ns for the circuit before irradiation. So, P- MOSFET influence on transient characteristics (rising time) is more important than that of N MOSFET because of summarized mobility reduction and threshold voltage value increasing. Both of these factors degrades P- MOSFET transconductance and results to a large rising time increasing.

N-MOSFET influence on transient characteristics (fall time) is less important - N-MOSFET transconductance degradation results to a little fall time increasing.

Reduced MOSFET transconductances reduce parasitic signal oscillations after rising/falling processes - oscillating amplitude $U_{max_oscill_1}$ reduces from 0.6V to 0.4V (for 300krad) after falling process and from 0.7V to nearly zero after rising process. That's good in the context of SI analysis.

To analyze $\Delta U/\Delta t$ (parameter for IBIS model [7]

without PCB influence) degradation after irradiation, buffer output signal waveforms were simulated for resistive load $R=50$ Ohm . The simulated waveforms are presented in Fig. 6.

It is seen that for 50 Ohm load irradiation results not only to rising/falling slew rates reduction (for example, falling slew rate $\Delta U_1/\Delta t_1$ reduces from 1.4V/ns to 0.4V/ns) but to reduction of maximal output voltage value U_{max_out} from 3.2 to 1.18V.

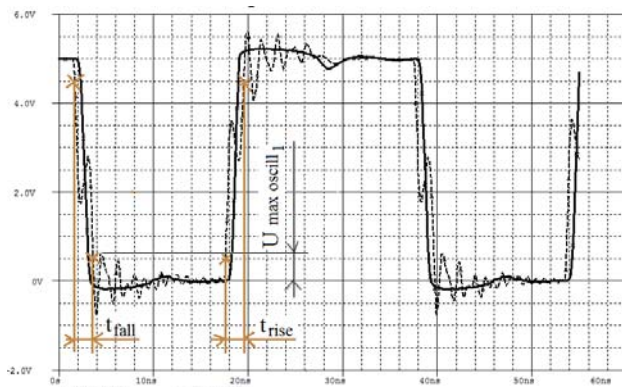
IV. CONCLUSIONS

1. It was shown that it is necessary to take into account the radiation influence on CMOS IC's waveforms distortions in SI analysis for PCB traces of satellite communication systems.

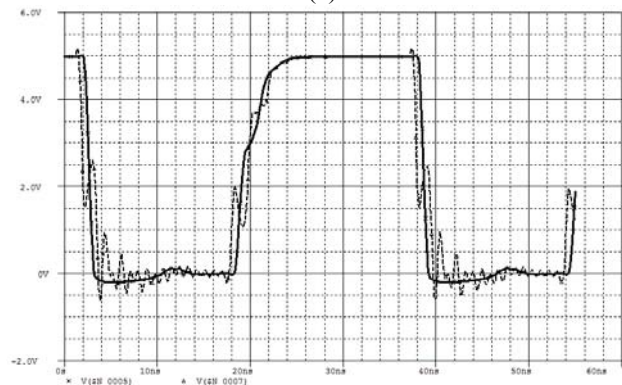
2. The simulation results showed that rising/falling waveforms degraded after irradiation. Herewith P-MOSFET influence on CMOS IC output transient characteristics degradation (rising waveforms) is more important (more than 4 times in comparison with the circuit before irradiation) than that of N-MOSFET (falling times increased in 17%) because of summarized P-MOSFET mobility reduction and threshold voltage value increasing.

3. Reduced transconductances of N- and P-MOSFETs reduced parasitic oscillations in PCB trace. That's positive result in the context of SI analysis.

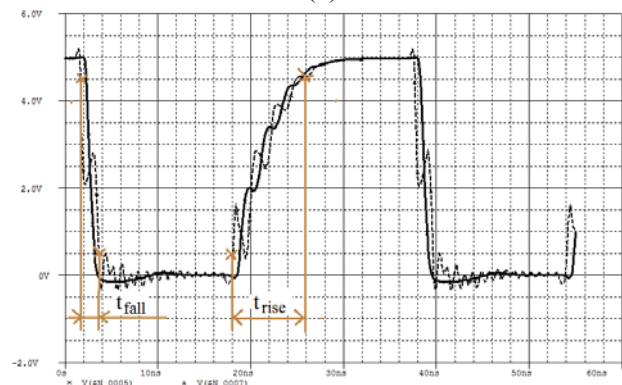
4. For 50 Ohm load irradiation resulted to rising/falling times increasing and to reduction of maximal output voltage value .



(a)

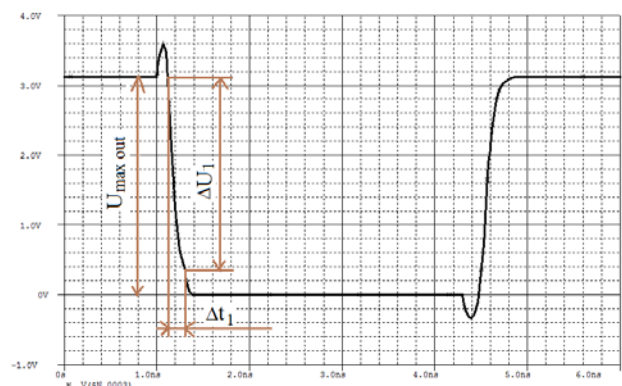


(b)

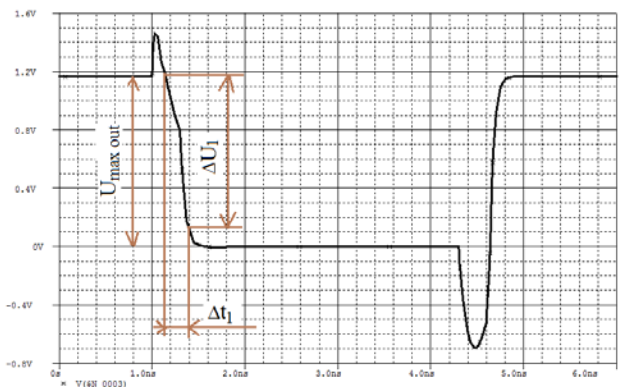


(c)

Fig. 5. SPICE simulated output buffer signal waveform (solid line) and receiver input signal waveform after PCB trace (dashed line) before (a) and after 100krad dose (b) and 300krad dose (c). Trace length is 100 mm



(a)



(b)

Fig. 6. SPICE simulated output buffer signal waveform (solid line) for load 50 Ohm before (a) and after 300krad dose (b).

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